

A symmetrical, planar SOFC design for NASA's high specific power density requirements

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Abstract

Solid oxide fuel cell (SOFC) systems for aircraft applications require an order of magnitude increase in specific power density (1.0 kW kg^{-1}) and long life. While significant research is underway to develop anode supported cells which operate at temperatures in the range of $650\text{--}800^\circ\text{C}$, concerns about Cr-contamination from the metal interconnect may drive the operating temperature down further, to 750°C and lower. Higher temperatures, $850\text{--}1000^\circ\text{C}$, are more favorable in order to achieve specific power densities of 1.0 kW kg^{-1} . Since metal interconnects are not practical at these high temperatures and can account for up to 75% of the weight of the stack, NASA is pursuing a design that uses a thin, LaCrO_3 -based ceramic interconnect that incorporates gas channels into the electrodes. The bi-electrode supported cell (BSC) uses porous YSZ scaffolds, on either side of a $10\text{--}20 \mu\text{m}$ electrolyte. The porous support regions are fabricated with graded porosity using the freeze-tape casting process which can be tailored for fuel and air flow. Removing gas channels from the interconnect simplifies the stack design and allows the ceramic interconnect to be kept thin, on the order of $50\text{--}100 \mu\text{m}$. The YSZ electrode scaffolds are infiltrated with active electrode materials following the high-temperature sintering step. The NASA-BSC is symmetrical and CTE matched, providing balanced stresses and favorable mechanical properties for vibration and thermal cycling.

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1. Introduction

Solid oxide fuel cells (SOFCs) have tremendous commercial potential due to their high efficiency, high energy density, and flexible fuel capability, operating on both hydrogen and hydrocarbon-based fuels. The materials and fabrication challenges of SOFCs have had a long development history and are well documented in several review articles [1,2]. The SOFC design is critical in the success of any new technology. The design impacts essentially every step of the development process: the options available for component fabrication, the maximum cell size, cell to interconnect contact and internal resistive losses, electrolyte thickness, cell performance, cell

stresses and robustness, temperature gradients, fuel distribution, gas manifolds, seals, degradation mechanisms, manufacturing reproducibility and yields of parts, and ultimately all of these impact the economics. A poor initial cell design can lead down a road of continual short-term fixes that can significantly limit the progress in scale-up while increasing the time and cost to commercialization.

The materials challenges for SOFCs are numerous and after 50 years of development the technology with the most demonstration experience is the Siemens Westinghouse (SW) tubular stack design. There are a number of reasons why the SW design is successful; the electrolyte is very thin, the ceramic interconnect, Ca-doped LaCrO_3 (LCC), is very thin and stable, and the tubular design does not require seals, an issue very problematic in planar designs. However, despite the success of the SW system the large tubular design has a relatively low specific power density (kW kg^{-1}) that lead developers to begin looking at smaller, planar cell and stack designs, where cells could be stacked more compactly, to achieve higher specific power densities.

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Planar design concepts were faced with two key initial challenges compared with the SW tubular technology: (1) they required high-temperature seals and (2) they required that the LCC interconnect be much thicker and that it contain some pattern of channels for gas distribution. LCC interconnects initially required sintering in reducing environments at high temperatures to achieve full density [3] but years of materials research lead to sintering aids which allowed the LCC interconnect to be sintered in air atmospheres, to full density, at temperatures below 1500 °C. Unfortunately it was eventually discovered that the oxygen partial pressure gradient across the thick LCC plates created sealing and thermo-mechanical stress problems. Ultimately the high cost of the LCC raw materials and difficulties in manufacturing relatively complicated parts within precision tolerances, influenced developers to abandon the ceramic interconnects in favor of metallic interconnects.

Metal interconnects created new materials challenges; oxidation rates were too high at 900–1000 °C and the operating temperature was soon dropped to 850 °C. Also, metal interconnects had a higher coefficient of thermal expansion (CTE) than YSZ, making the YSZ/metal interconnect seal difficult to maintain under thermal gradients and during thermal cycles. Even at the reduced temperature of 850 °C it was discovered that the Cr₂O₃ scale, which protected the metal interconnect, was gradually volatilizing Cr-based vapor species, which were then depositing at the cathode [4]. In addition, the metal/cell contacts or repeat unit (RU) contact resistance became an increasing issue as the stack temperature was lowered. Curvature of the cells, when joined with the interconnects, can result in gaps of 100–200 μm [5], which must be bridged with conductive pastes (or other technique), normally deposited during manual stack assembly. It is also a common practice to deposit thin coatings on the metal interconnects, designed to reduce Cr-evolution and to improve the conductivity, composition or morphology of the oxide scale that is formed. With time at high temperatures and with thermal cycling, it is possible for the coatings to form micro-cracks and to eventually spall, leading to higher contact resistance.

Recent studies indicate that Cr-poisoning is so prevalent at 850 °C that current planar stack development is being driven towards operating temperatures below 750 °C for continuous operation with acceptable cathode degradation [6]. While reductions of cell temperature do alleviate several chemically induced degradation mechanisms, these reductions come at the cost of significantly reduced cell performance as shown in Fig. 1. Reducing the temperature from 850 °C to 700 °C results in a drop in power density from 900 mW cm⁻² to 300 mW cm⁻² (at constant voltage), a 75% decrease [7]. While recent advances in materials and microstructure have improved low-temperature operation, the power potential of SOFCs at higher temperature is being largely disregarded.

While small (2–3 cm²) anode supported cells (ASC) have shown impressive performance, as high as 2 W cm⁻² [8], the power losses for full sized cells, due to various polarizations, can exceed 40% in a multi-cell planar stack, reducing the actual performance per cell to 0.4–0.6 W cm⁻² [9]. A number of problems contribute to this loss in power depending on the stack

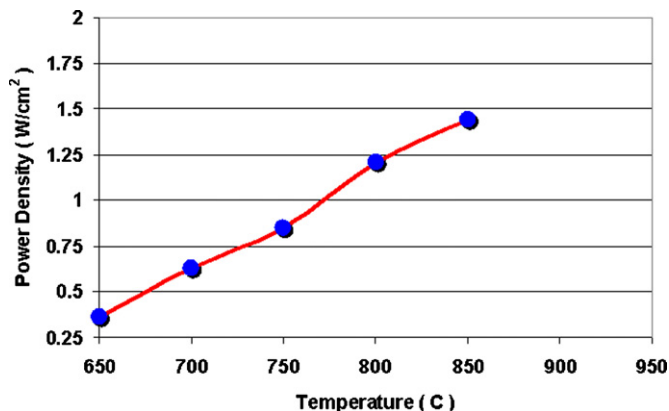


Fig. 1. SOFC button cell performance with temperature at 0.7 V.

design; some are unavoidable such as gradients in temperature and fuel composition, but can be minimized by stack design. In spite of the long list of materials problems, significant progress is being made by the industrial teams in the DOE/SECA program. Some developers are beginning to report stack performance at 800 °C, approaching the DOE/SECA target of 0.2 kW kg⁻¹ for the stack [10]. While these levels of performance may be suitable for stationary power and ground transportation, the requirements for aeronautic and aerospace applications exceed specific power densities of 1.0 kW kg⁻¹, nearly five times the current technology levels [11]. With metal interconnect content comprising greater than 75% of the stack weight, and current developers lowering temperatures to below 750 °C, the challenge of increasing a cell designed for 0.2 kW kg⁻¹ to greater than 1.0 kW kg⁻¹ seemed insurmountable in the near future [12]. The use of metallic interconnects, thus limiting the temperature, and the use of cambered anode supported cell, thus limiting sealing options and cell/interconnect conductivity has led NASA to evaluate alternative cell and stack concepts to meet the demanding requirements of high power, low weight/volume, and robust thermal/mechanical performance.

2. The symmetrical, NASA bi-supported cell (BSC) concept

In developing a new cell concept NASA endeavored to address each of the key issues encountered by the state-of-the-art SOFC designs; the need to reduce the temperature, poisoning by Cr, oxidation/reduction of the anode, the weight of the interconnect, cell/metal contact resistance, and seals. To reach the highest possible power capabilities of SOFCs NASA chose to go to higher temperatures and to evaluate an all ceramic stack concept, using LaCaCrO₃ (LCC) or other doped-LaCrO₃ interconnects, rather than metal interconnects. The technical approach taken by the NASA design for each of the major hurdles discussed above is shown in Table 1. The key feature of the design concept is the symmetrical cell, which is made by supporting the thin electrolyte on both sides with a porous YSZ support structure, thus the name bi-supported cell or BSC.

The porous YSZ support structure (scaffold) has graded pores, so the gas channels or flow fields may be incorporated

Table 1
NASA-BSC technical approach to major materials challenges

Materials challenges of anode supported cells with metal interconnects	NASA Glenn all ceramic design and fabrication advantages
Developing a thin, supported, YSZ electrolyte to compensate for reduced operating temperature and avoiding Ni–NiO oxidation/reduction	Thin YSZ electrolyte is supported by porous YSZ electrodes on both sides, symmetrical and balanced stresses. All ceramic allows high-temperature operation
Metal to ceramic seals perform well at operating temperature but thermal cycles a challenge, allowing about 2% leakage of fuel	Integral YSZ seals are applied in the green state, matched CTE with the thin LCC interconnect and sintered with stack to form hermetic seals
Contact resistance between the metal interconnect/cell, design must compensate for curvature of the cells	Cell and interconnect sintered together to form a “unitized” repeat unit, electrode infiltration ensures contact between anode/cathode and LCC interconnect
Evolution of Cr-species from the metal interconnect most likely requires pretreatment and/or coatings	LCC interconnect does not evolve Cr-species, it is stable in SOFC operating conditions

into the electrodes rather than the interconnect, thus significantly reducing the weight by allowing a thin, LCC interconnect. The graded porosity provides the smallest pores (1–5 μm) at the electrode/electrolyte interface, creating the maximum amount of active interfacial area or triple phase boundary (TPB) once the active electrodes are infiltrated. The continuous pore channels (tortuosity equivalent to 1) then increase in size to approximately 80–100 μm in diameter and serve the role of gas flow fields for air and fuel. While there are numerous fabrication techniques commonly used in ceramic processing that can generate a graded porous structure [13], NASA has modified and developed a novel processing technique, based on freeze-tape casting, to create previously unachievable pore morphologies in thin film ceramics. The thin electrolyte is deposited and sandwiched between two layers of identical green tape so it is balanced on either side with identical YSZ support structures or electrode scaffolds as shown in Fig. 2. If the single cell is sintered at this stage it has the advantage of containing only YSZ, no other material, and of being symmetrical about the central electrolyte, which allows the part to sinter flat since the stresses are low and balanced equally on both sides of the thin electrolyte.

Given the *iso*-material design of the cell and the ability to co-sinter the green unit as one assembly, the fabrication of the BSC repeat unit is relatively simple. A BSC stack is fabricated

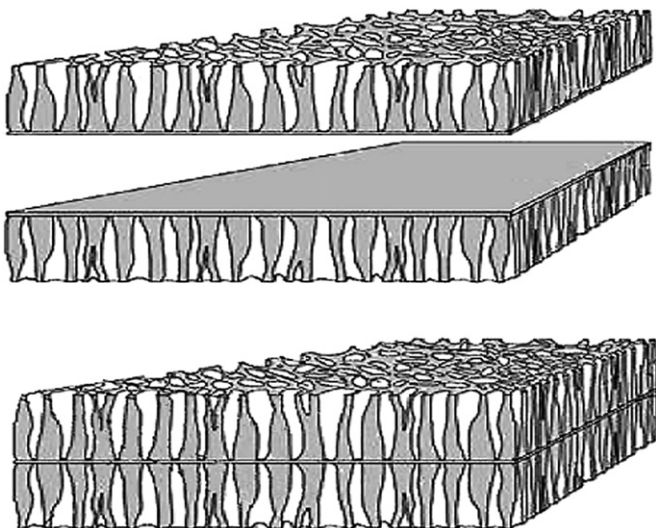


Fig. 2. Thin YSZ electrolyte supported between two graded porous scaffolds.

by coating one face of the cell with a thin layer of LCC, on the order of 30 μm , producing a repeat unit, followed by applying the thin YSZ edge-seals as shown in Fig. 3 with simple cross-flow geometry. The LCC layer must be thick enough to bridge the 80–100 μm channels of the cells connected on either side, and to result in a dense and completely hermetic layer following the sintering process. Multiple repeat units can be laminated in the green state to produce a multi-cell BSC stack. Deposition of the LCC is performed just like the electrolyte layer, by screen printing or air brushing over the large pores of the YSZ scaffold. One of the advantages of fabricating a number of cells into a stack using this method is that only the YSZ and LCC materials are fired in the high-temperature sintering step. Not only is it easier to optimize the sintering and shrinkage rates of only two discrete materials systems with similar thermal expansions, both materials are conducive to 1400 $^{\circ}\text{C}$ firing schedules with negligible chemical interaction.

The edge-seals are made of YSZ and can be deposited in the green state using the same YSZ composition and particle size as the electrolyte. During sintering the three components of the multi-cell stack, the YSZ electrolyte, LCC interconnect, and YSZ edge-seals, shrink to high density (no open porosity) thus forming hermetic seals. Since the structural base of the cell has

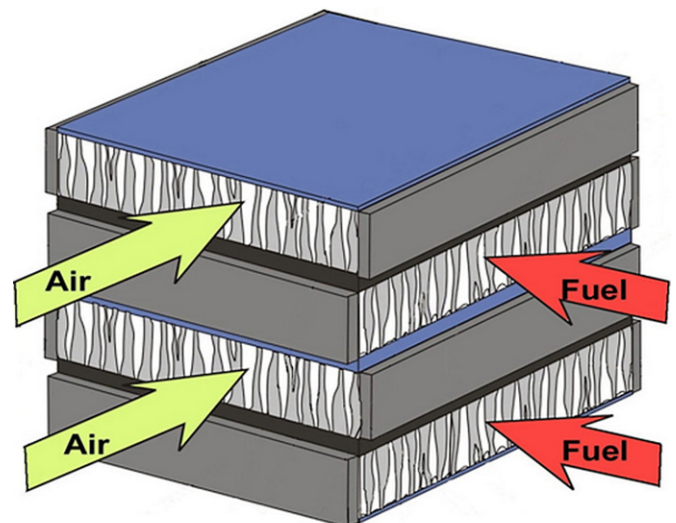


Fig. 3. BSC cross-flow stack.

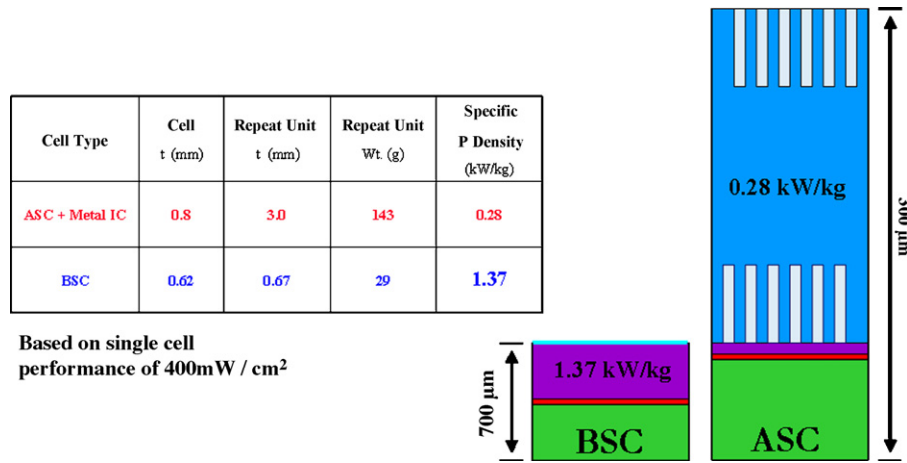


Fig. 4. Comparison of BSC vs. ASC specific power density, based on a single repeat unit with a $10\text{cm} \times 10\text{cm}$ cell.

been prepared with a single high-temperature processing step the “unitized”, multi-cell stack can be leak tested and mechanically tested before further processing of electrode materials, which can minimize costs in scale-up and wasted electrode precursors. One of the most straightforward designs is the cross-flow design as is shown in Fig. 3. Electrodes may be deposited by liquid or vapor chemistry techniques using the gas flow channels as a delivery vehicle, with the final goal being the complete coating of all exposed interfaces within the electrode and gas channels. As the electrode materials are deposited at the YSZ electrolyte/electrode interface, on the walls of the channels, and on the surface of the exposed LCC interconnect, they produce a continuous, low resistance, electrical path from the electrolyte interface, to the LCC interconnect and to the next cell, thus minimizing ohmic losses through the repeat unit. At present, multiple infiltrations are required, resulting in a continuous and even deposition of electrode material.

If the BSC stack concept can be manufactured into the “unitized” stack as has been described, it could represent a substantial increase in specific power density. Comparing two repeat units (RU) with $10\text{cm} \times 10\text{cm}$ cells, considering all 100cm^2 area to be active, one RU made with metal interconnects and anode supported cells and the other RU using the BSC concept, both operating at 850°C with a moderate power density of 400mW cm^{-2} , the ASC/metal interconnect RU would weigh approximately 143 g and the BSC RU would weigh approximately 29 g. Using those assumptions the specific power density for the ASC/metal interconnect RU is 0.28 kW kg^{-1} and the BSC RU is 1.37 kW kg^{-1} as shown in Fig. 4. The power per volume is also greatly improved, being 1.3 kW L^{-1} for the ASC and increasing to 9.3 kW L^{-1} for the BSC. While there are optimistic assumptions made for this calculation it does however show the large potential in specific power that might be gained from altering the stack design, by and large by removing the heavy metal interconnects. Of added significance is the operating temperature, the ASC stack cannot operate at 850°C with the heavy use of metals, while the BSC stack should be able to operate at higher temperatures, greater than 900°C if required, thus providing even higher power density. NASA GRC is in the process of evaluating, at the bench scale, the all ceramic,

symmetrical BSC concept by fabricating cells, repeat units, and eventually small stacks as discussed in detail below.

3. Initial fabrication trials

Initial fabrication of the BSC NASA took advantage of a new tape casting technique called freeze-tape casting, the details of which are the subject of another paper. In freeze-tape casting an aqueous or organic slip is cast across a freezing bed and micron size ice crystals start to form at the Mylar side of the tape and they gradually grow larger and larger towards the top as shown in Fig. 5. The freezing process creates a natural gradient in porosity in the green tape and subsequently the ice crystals are removed by sublimation in a vacuum. Symmetrical cells were fabricated by taking two green parts cut from the same or similar freeze-tape cast substrates, depositing a thin electrolyte layer between the tapes, and laminating the tapes together with the small pores

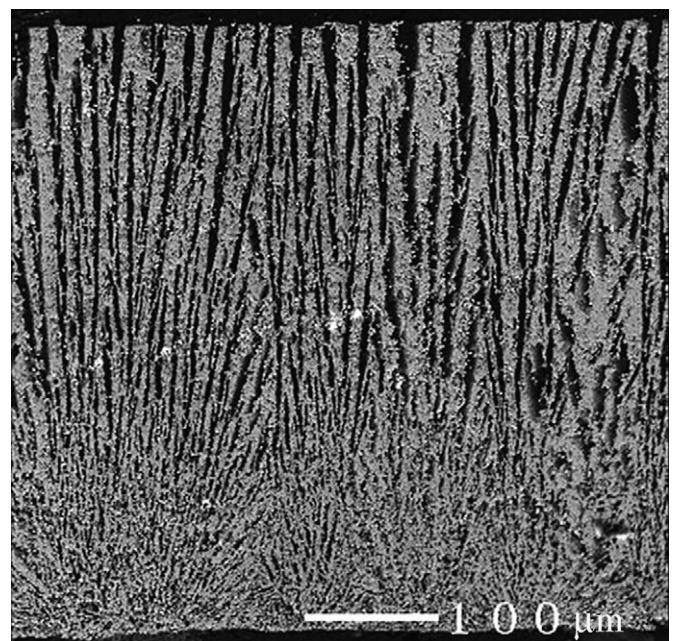


Fig. 5. Single graded porous electrode scaffold.

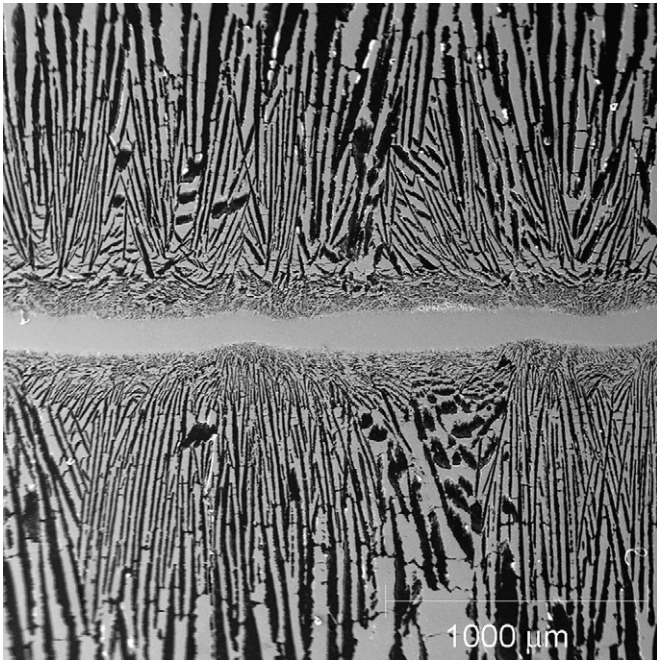


Fig. 6. Cross-section of sintered BSC prior to electrode infiltration.

facing each other, forming the YSZ tri-layer. Symmetrical cells, with pinhole-free electrolytes, have been successfully fabricated as shown in Fig. 6. The electrolyte layer can be formed by a number of ceramic processing techniques including air brushing, screen printing, and other green ceramic fabrication techniques. The interface between the electrolyte and the small pores of the porous electrode scaffold, due to the inherent green state porosity formed with freeze-tape casting, create an ideal rough interface for bonding, thus maximizing the length of the triple-point-boundary (TPB).

A strong benefit of the symmetrical cell design and *iso*-structural use of YSZ for the entire tri-layer is the co-firing process. Uniform and equal densification of each opposing scaffold creates a uniform stress field with the electrolyte at the center. The result is a solid oxide fuel cell that sinters flat with no net curvature on either side of the electrolyte. Fig. 7 illustrates the fabrication of a 19 cm diameter flat tri-layer fuel cell that was sintered free, without additional bisque firing steps or creep flattening procedures, and without the use of a weighted



Fig. 7. Large area (285 cm² active area) YSZ-based BSC fabricated at 1400 °C.

cover plate. This capability allows for potential scale-up to large diameters that can further increase the specific power density of an SOFC system.

Initial electrode infiltration techniques have used Ni-nitrates or stoichiometric solutions of nitrates for the cathode. These nitrates were then infiltrated into the YSZ electrode scaffold, without the use of a vacuum, and were allowed to dry/solidify prior to heat treatment in both air and reducing atmospheres for decomposition of the nitrates into metals or metal oxides depending on the electrode. This infiltration procedure was performed multiple times on both the anode and cathode to achieve suitable electrode coverage as evaluated with SEM analysis. The final BSC is calcined at 600 °C to ensure coalescence of materials from separate infiltration steps. Fig. 8 is a high resolution scanning electron micrograph of porous YSZ electrode scaffolds infiltrated with active electrodes.

To evaluate fabrication techniques for future development of repeat units and stacks, studies were initiated on LCC/YSZ bi-layers, and YSZ/LCC/YSZ tri-layers to initiate matching of

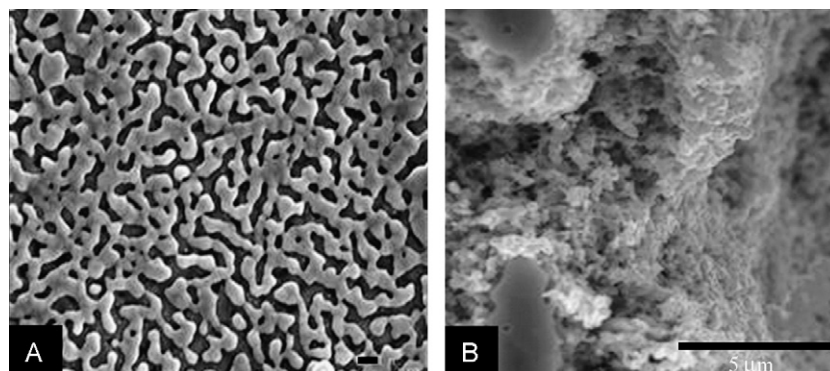


Fig. 8. Infiltrated electrode scaffolds with continuous (A) nickel metal anode (bar = 1 μm) and (B) lanthanum strontium ferrite LSF cathode (bar = 5 μm).

shrinkage curves and CTE. Custom LCC powders were synthesized and characterized at NASA to achieve sintering shrinkages and densities similar to that of YSZ at 1400 °C. Application of the thin LCC interconnect was performed in a similar way to the electrolyte layer, by screen printing, air brushing, or other technique, with the properties and viscosity of the ink/paint tailored for deposition over the large pores of the YSZ scaffold.

4. Discussion and future work

In all SOFC designs there are fabrication and materials challenges to overcome, the major BSC challenges are listed in Table 2. The two most significant hurdles to this new design concept encompass the ability to effectively infiltrate stable electrodes at low weight percents as well as the issue of gas flow through the pore channels in a functioning cell and stack. The electrode infiltration may result in a multi-step process, although these processing steps may only require low-temperature heat treatment, on the order of 400 °C. In a multi-step process it is important that the micro-channels not be blocked or that larger amounts of electrode fill the bottom of the channels, an effect of pooling of the electro-catalyst infiltrant, while leaving channels or walls relatively sparse and uncoated. It is also important that the electrode maintain a continuous path, with low resistance, from the electrolyte interface to the interconnect, in order for the electrode not to add any significant resistance to the cell ASR. In particular, fabricating the nano/micro-structured metallic anode and preventing the Ni-metal phase, or other metals, from sintering could prove to be a long-term hurdle but a potential opportunity for use of new highly conductive ceramics.

Gas flow through the BSC channels, however, given the absence of line of sight or other simple pattern of channels or added interconnect designs, is without question the largest challenge to this design and it has been recognized from its initial concept. Fuel and air back pressures could be very high due to the tortuous path created by YSZ support structures with graded porosity; this has an added adverse effect on the ability of the cell to remove heat. Studies have been initiated at NASA GRC to quantify the permeability of the BSC flow fields fabricated by freeze-tape casting. A test rig has been fabricated to measure back pressure as a function of gas flow rate and samples are in the process of being tested, with variations in electrode thickness and with steps taken to modify and improve the directionality of the flow channels. Initial fabrication has been done with freeze casting small lengths of electrode tape, as the process is scaled up increasing the alignment of the channels will

improve. Modeling of the cell, including fuel utilization, temperature distribution and back pressure is also in progress and will be discussed in a follow-up paper, which will also discuss the electrochemical cell performance.

5. Conclusions

A novel, symmetrical cell architecture, the bi-electrode supported cell (BSC) and a compact stacking design has been developed to achieve the high specific power densities required by the aeronautics industry and for many other applications. Our calculations, described above, show that the BSC cell/stack design is capable of achieving five times the power density and reaching the NASA target of 1.0 kW kg⁻¹, without requiring increases in power density at the cell level. The symmetric structure allows cells to be sintered at high-temperature without shrinkage mismatches that can lead to curvature and/or residual stresses. Because fuel and air are manifolded through the thick scaffold electrodes, the interconnect does not require integral gas flow channels. Thus, interconnect thicknesses are greatly reduced, providing enormous increases in gravimetric and volumetric power density compared to existing anode-supported planar stack designs, making the BSC concept ideal for development of SOFC-based auxiliary power systems for jet aircraft.

The BSC concept was designed with ceramics processing and scale-up in mind. All the fabrication processes are common in MLC processing and should be able to be scaled-up while maintaining low cost. Other anticipated benefits not discussed are robustness, allowing the stack to thermal cycle with less internal stresses and the flexibility of infiltrating non-traditional or multi-component electrode compositions by way of the liquid infiltration, while not affecting the CTE of the cell. As we proceed with development of aerospace APUs based on the BSC technology, there are a number of opportunities that will present themselves as the technology evolves. In addition to aircraft APUs, in the 440 kW range, the BSC design is amenable to applications where small, portable power systems are required, like those by the military in the 20–50 W range.

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References

- [1] N.Q. Minh, *J. Am. Ceram. Soc.* 76 (3) (1993) 563–588.
- [2] S.C. Singhal, K. Kendall, *High Temperature Solid Oxide Fuel Cells: Fundamentals, Designs and Applications*, Elsevier Science, 2004.
- [3] L. Group, H.U. Anderson, *J. Am. Ceram. Soc.* 57 (150) (1974).
- [4] E. Konyshva, H. Penkalla, E. Wessel, J. Mertens, U. Seeling, L. Singheiser, K. Hilpert, *J. Electrochem. Soc.* 153 (4) (2006) A765.
- [5] J. Lanutti, W. Li, OSU, DoE SECA-CTP Review Mtg., Sacramento, CA, February 2003.

Table 2
Technical hurdles

Will the graded porosity of the porous YSZ electrode supports provide required fuel/air feed without large back pressure or will they need additional channels
Infiltration of the active electrodes into the porous gas channels from the edges
Electrode impregnation may require multiple steps to provide enough electrode material for low ASR
Can the Ni(m) be pinned into position to provide low sintering rate
Demonstration of a multi-cell stack with hermetic edge-seals and low ASR

- [6] Z. Yang, K.S. Weill, D.M. Paxton, J.W. Stevenson, *J. Electrochem. Soc.* 150 (9) (2003) A1188.
- [7] J. Stevenson, National Space and Missile Materials Symposium, PNNL, Seattle, Washington, 2004.
- [8] J.W. Kim, A.V. Virkar, K.Z. Fung, K. Mehta, S.C. Singhal, *J. Electrochem. Soc.* 146 (1) (1999) 69–78.
- [9] S. Shaffer, Proceedings of the 6th Annual SECA Workshop, Delphi, Pacific Grove, CA, 2005.
- [10] A. Liang, DOE SECA Conference, 2003, www.netl.doe.gov/publications/proceedings/03/seca/Anita%20Liang.pdf.
- [11] L.M. Anderson, A.K. Misra, Symposium on Novel Vehicle Concepts and Emerging Technologies (Paper No. 36), Brussels, Belgium, April 7–10, 2003.
- [12] M. Stelter, A. Reinert, B.E. Mai, M. Kuznecov, *J. Power Sources* 154 (2006) 448.
- [13] S. Sofie, F. Dogan, *J. Am. Ceram. Soc.* 84 (7) (2001) 1459–1469.